

# Eric Mariacher: SOFTWARE MANAGER

## Profile

17 years of experience in Embedded Software Projects in engineering, architect, project and functional management positions in Europe, US and India.

My goals are:

- \* to set-up the right environment for developers to build software.
- \* to give best visibility on software development process from requirement phase to field support phase.

## MAJOR ACHIEVEMENTS:

- Mariner Networks: Hiring a whole embedded software team.
- Logitech: Software development processes reengineering.
- IBM: Network management Java applets development.
- IBM: Responsible for hands-on formation of European network support for the 8265.
- BERATA/BMW/AUTOSAR: new members technical course building and delivery.

## Competencies

- **Team Builder (2 years).**
  - Hiring a whole embedded software team (5 developers - Mariner).
  - Starting embedded software activity (5 developers – BERATA)
  - ISO9000 compliant Developpement processes definition (Mariner).
- **Team Manager (4 years).**
  - Mariner 2 years 5 developers**
  - Logitech 2 years 8 developers**
    - Communication, resource allocation, evaluation...
    - Day to day people management.
    - New developer hiring.
    - planning.
    - dealing with external providers in US, europe and India
    - start-uping projects.
- **Team Leader for Software development processes re-engineering (4 years).**
  - Setting Configuration Management methodology in Switzerland and USA.
  - Setting Requirement Management methodology in Switzerland and USA.
  - Quality Assurance processes improvement
  - Architected improved firmware re-usability by promoting modularity.
- **Project Management (8 years)**
- **Software technical competencies (7 years):** C, assembler, real-time embedded software, Linux, [Java](#), Web.
- **Networking (7 years):** IP, ethernet, Bluetooth, ATM, ...
- **Microelectronics technical competencies (6 years):**
  - Analogic design (3 years 1989-1992):
    - mask design
    - spice simulations
    - power switching noise studies
    - test procedures definition
  - Logic design (2 years 1993-1995):
    - bus PCI
    - Development of a software model, simulating the core of the microcontroller.

# Eric Mariacher: EMBEDDED SOFTWARE MANAGER & ARCHITECT

16 December 1965, Married, 3 children, french/swiss double nationality. [eric.mariacher@gmail.com](mailto:eric.mariacher@gmail.com).

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<http://www.viadeo.com/public/profil/?memberId=002rw1qp9bm4c75>

[https://www.xing.com/hp/eric\\_mariacher](https://www.xing.com/hp/eric_mariacher)

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## Professional experience :

### 2004-2007 **Logitech** Swiss, Senior Technical Manager 3 years

#### \* 8 developers Functional Manager.

- Communication, resource allocation, evaluation...
- Day to day people management.
- New team members hiring.

#### \* **Wireless keyboards and receivers** Technical Manager

- planning.
- dealing with external providers (US, european and indian).
- start-uping projects.
- bluetooth and proprietary wireless protocols.

#### \* **Software development processes re-engineering**

- Setting Configuration Management methodology in Switzerland and USA.
- Setting Requirement Management methodology in Switzerland and USA.
- Architected improved firmware re-usability by promoting modularity.

### 2002-2003 **BERATA** Swiss, Senior consultant 2 years

#### \* **Parallel projects leading for different clients.**

### 2003-2004 **AUTOSAR** 8 months

#### \* **AUTOSAR Spokesperson technical assistant.**

- AUTOSAR new applicants management.
- AUTOSAR new members technical course building and delivery.

### 2002-2003 **Schindler** 1 year

#### \* **Elevator Door Controller for new Schindler 700 platform.**

- Software Architecture review.
- Development of a proprietary protocol for validation and field support phases.

### 2000-2002 **Mariner Networks** embedded software manager & architect 2 years

#### \* **Mariner Networks France software team creation.**

- Team (5 engineers) Recruiting and managing.
- Development processes definition (ISO9000).
- Configuration Management tools study for a worldwide environment (France, California).

#### \* **Dexter 3000 quad ethernet module software architect and team leader.**

- STP, VLAN, hardware IP forwarding, ethernet<->ATM interface.

#### \* **VoATM/DBCES architect and team leader.**

- DBCES stack development.

### 1989-2000 **IBM** hardware & embedded software developer 11 years

-1999-2000 IBM Research Center, La Gaude France. (9 months).

#### \* **Architecture of a fully portable solution for **Prizma** switching chip control.**

- pSOS, Linux evaluated on a MPC860 & PPC405 PowerPC platform.
- Project management, both technical and human.
- Definition and development of OS portable code.

-1996-1999 IBM Research Center, La Gaude France. (4 years).

\* **Definition and Conception of the embedded system of the 8265 ATM switching hub family.**

- Team leader for the development of the boot (PowerPC 860 & 750).
- PCI interface driver.
- 8265 embedded web server, Network management Java applets development.
- Resolution of client's problems, ATM-PNNI network building.
- Responsible for hands-on formation of European network support for the 8265.

-1994-1995 IBM Development laboratory, Corbeil-Essonnes France. (2 years).

\* **Definition and conception of the 403GX PowerPC microcontroller.**

- Development of a software model, simulating the core of the microcontroller.
- Logic design, debug and synthesis of the interrupt controller pci/core interface.

-1993-1994 IBM Development laboratory, Raleigh NC USA. (1 year).

\* **Conception of the 403GA PowerPC microcontroller.**

- Development of user interfaces and tools for microcontroller simulation (debug and validation).
- Responsible for the automatic generation of test programs for the microcontroller validation.

-1989-1992 IBM Development laboratory, Corbeil-Essonnes France. (3 years).

\* **Analog Circuit and Physical design.**

- stand-alone CMOS/BiCMOS SRAMS and caches, analogic circuits (drivers, voltage references, studies on simultaneous switching in SRAMs...).
- Responsible for the definition and users/developers relations for a SRAM design application.

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**Other experiences :**

- 1988-1994 Privée de Gestion et de Conseil (Bank), Paris. Conception, development and support of a solution for stock exchange values management.
- 1988 IBM, Micro-electronic course and recruited at the end of this course.
- 1987 Merlin-gérin, Computer science and electronic student course on power supplies (Grenoble France).

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**Formation:**

- 2000 project management <http://www.pmi.org>
- 1983 Baccalauréat Paris
- 1988 Engineer diploma of the Institut Supérieur d'Electronique de Paris - [ISEP](#) (micro-electronic option)

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**Foreign languages :**

French, mother tongue.  
English, bilingual.  
German, current.  
Chinese.